

FIG. 1
-PRIOR ART-

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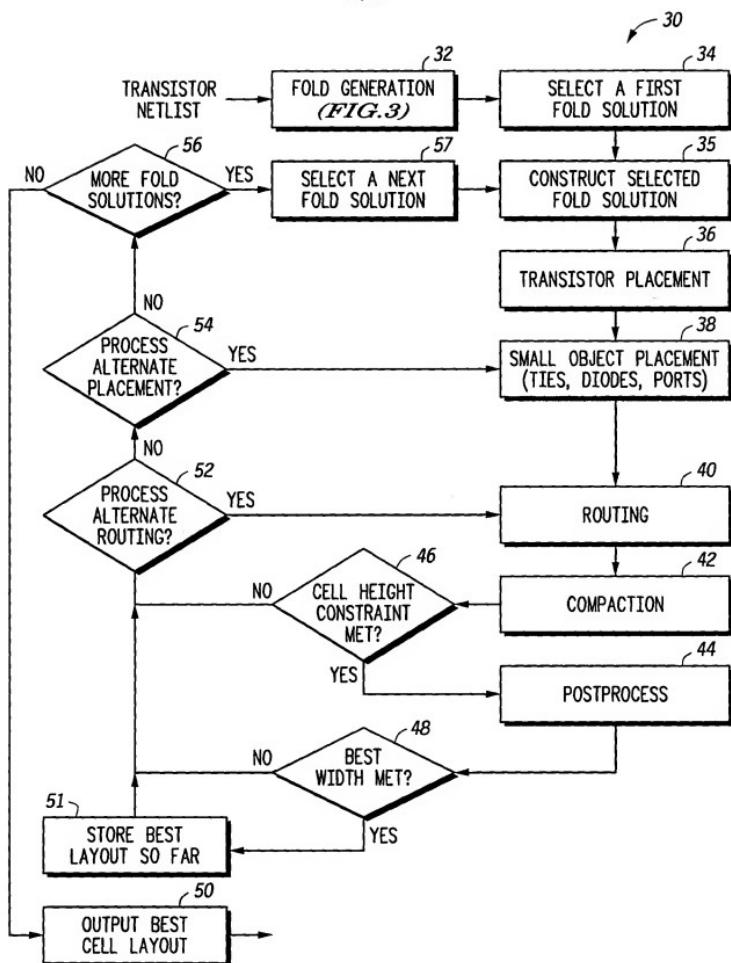


FIG.2

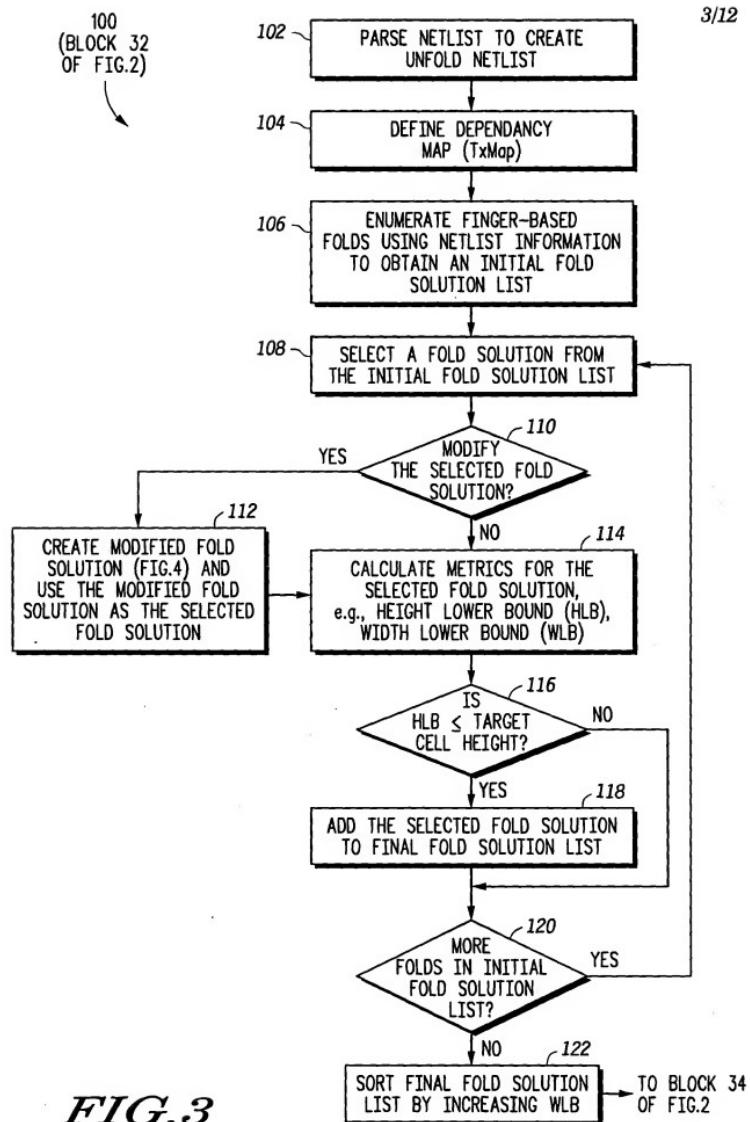


FIG.3

Γ

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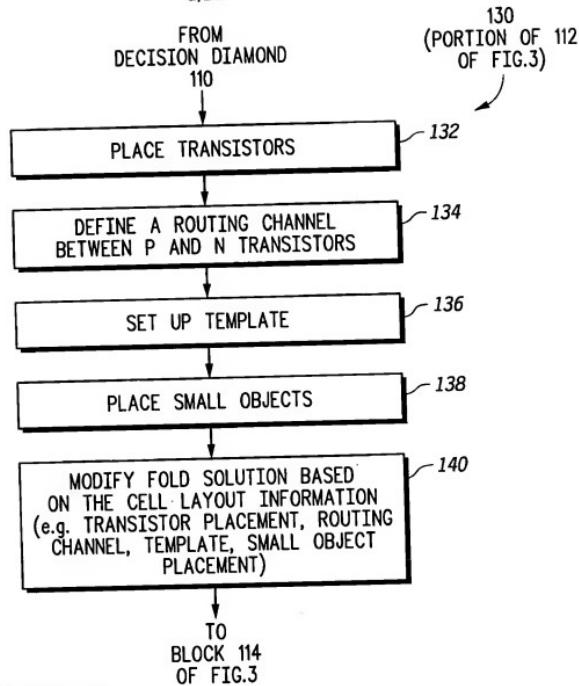


FIG.4

P TRANSISTOR WIDTH:

9	9	7	7	5	3
<u>253</u>	<u>254</u>	<u>255</u>	<u>256</u>	<u>257</u>	<u>258</u>

250

263	264	265	266	267	268	269
4	4	6	8	8	10	10

252

N TRANSISTOR WIDTH:

13 13 13 15 13 13 10

P + N TRANSISTOR WIDTH:

13 13 13 15 13 13 10

FIG.5

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FROM
BLOCK 104
OF FIG.3

150
(PORTION OF BLOCK
106 OF FIG.3)

MAKE INDEPENDENT FINGER DRIVEN
FOLDING OF P TRANSISTORS
TO CREATE PIND FOLD LIST
(FIG. 7)

152

MAKE FINGER DRIVEN FOLDING OF
N TRANSISTORS DEPENDENT ON P
TO CREATE NDEP FOLD LIST
(FIG. 8)

154

COMBINE PIND FOLD LIST AND NDEP
FOLD LIST TO CREATE A PIND/NDEP
FOLD LIST
(FIG. 9)

156

MAKE INDEPENDENT FINGER DRIVEN
FOLDING OF N TRANSISTORS
TO CREATE NIND FOLD LIST
(FIG. 7)

158

MAKE FINGER DRIVEN FOLDING OF
P TRANSISTORS DEPENDENT ON N
TO CREATE PDEP FOLD LIST
(FIG. 8)

160

COMBINE NIND FOLD LIST AND PDEP
FOLD LIST TO CREATE A NIND/PDEP
FOLD LIST
(FIG. 9)

162

MERGE PIND/NDEP FOLD LIST AND
NIND/PDEP FOLD LIST TO CREATE
THE INITIAL FOLD SOLUTION LIST

164

TO
BLOCK 108
OF FIG.3

FIG. 6

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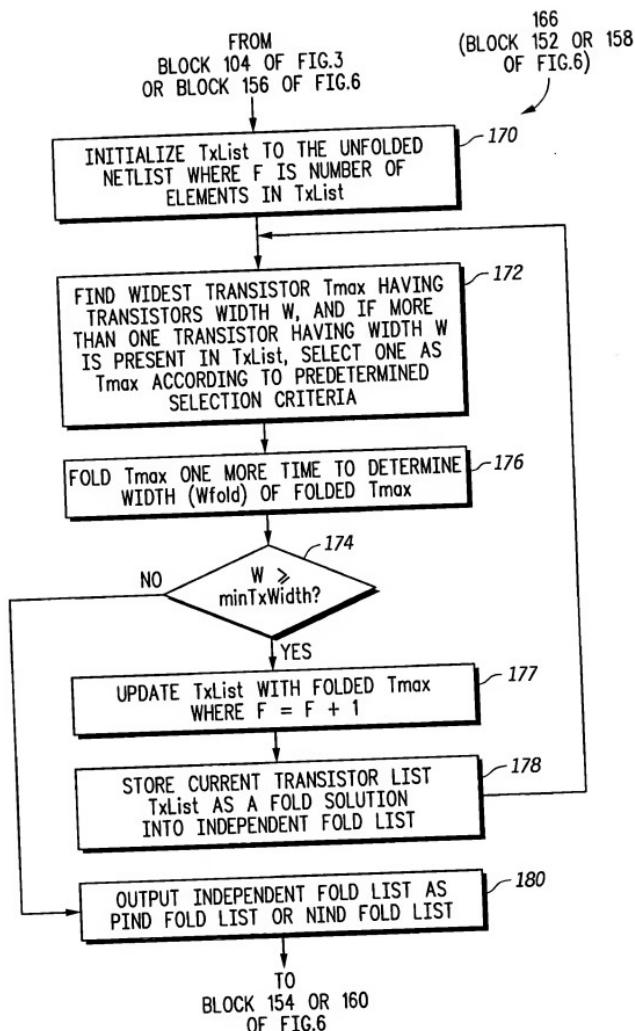
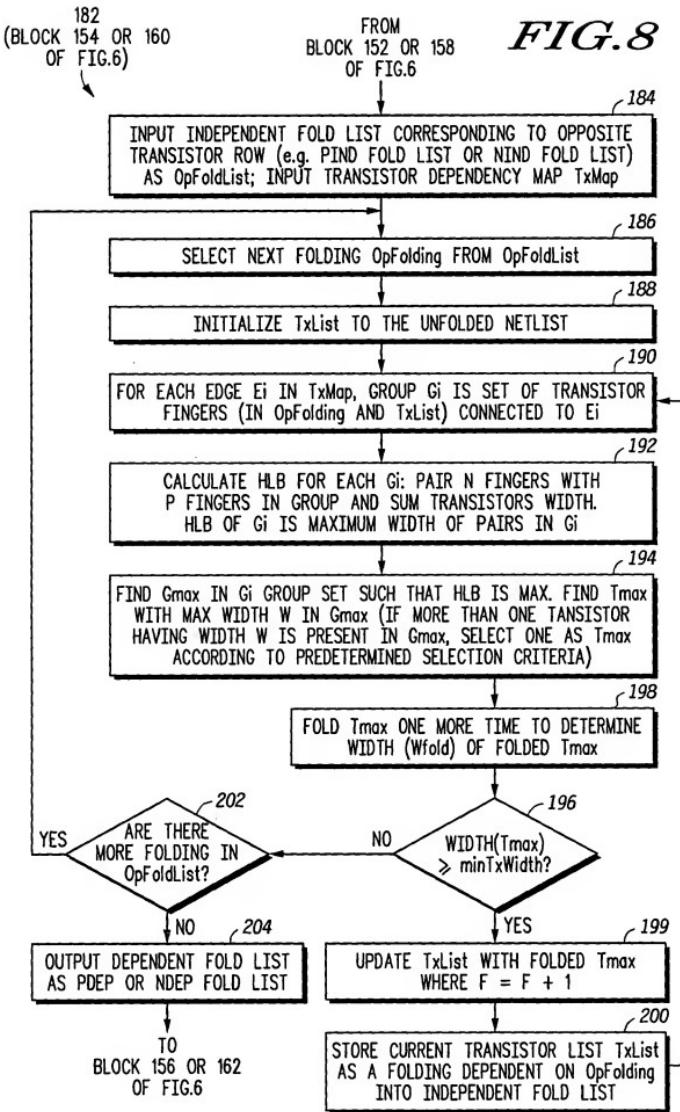


FIG. 7

FIG. 8

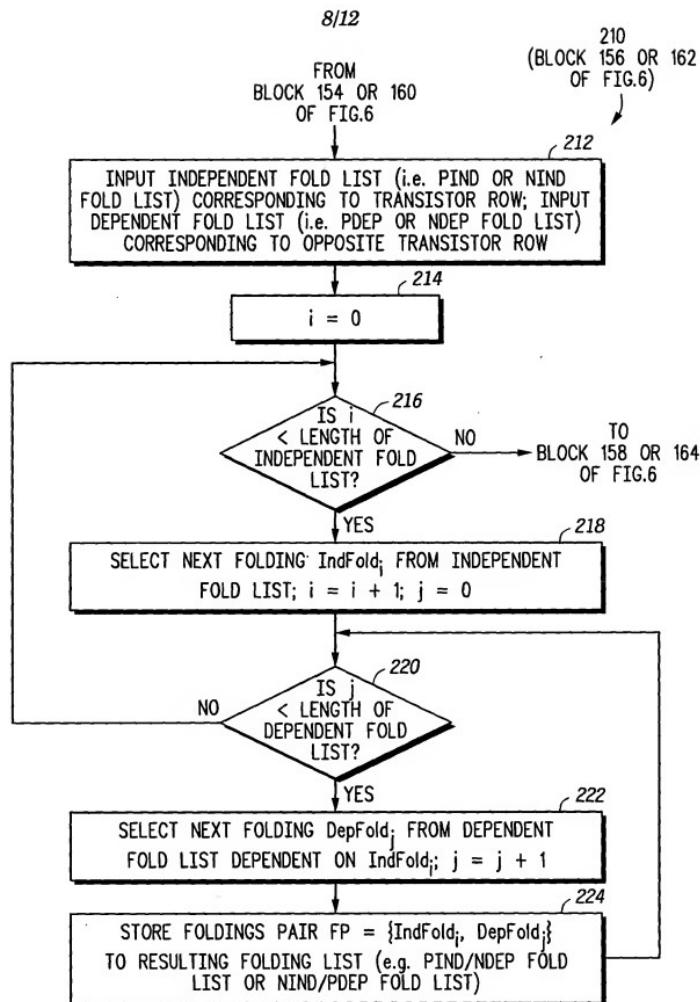


FIG.9

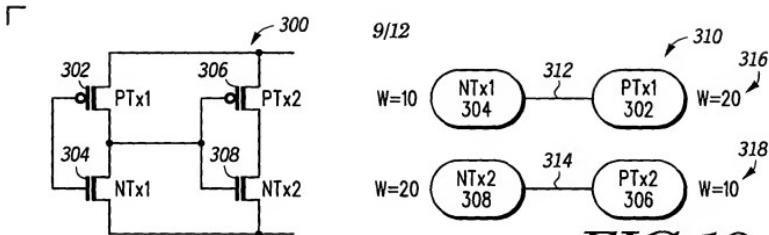


FIG. 10

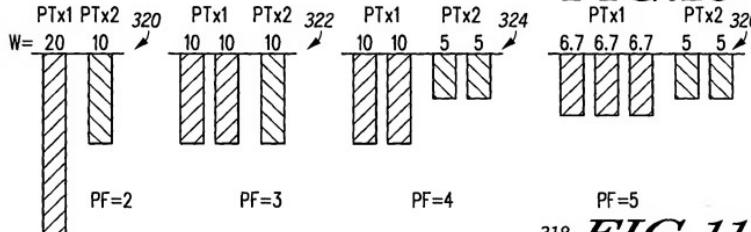


FIG. 11

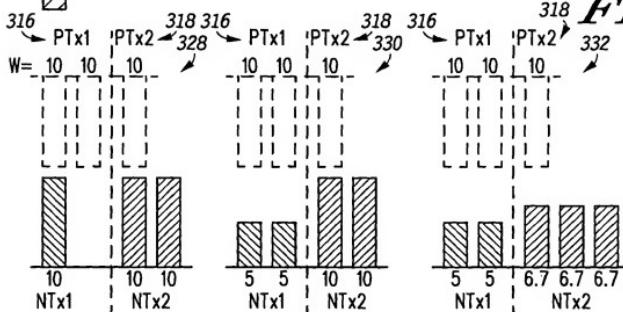


FIG. 12

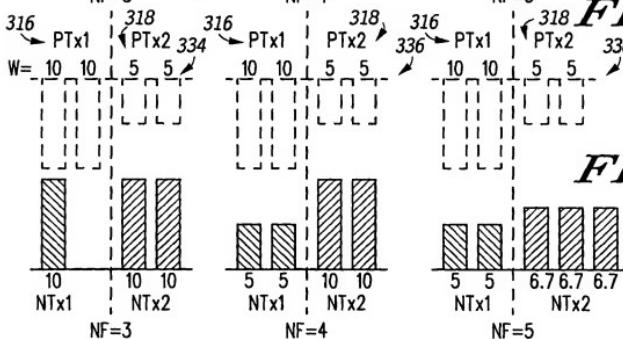
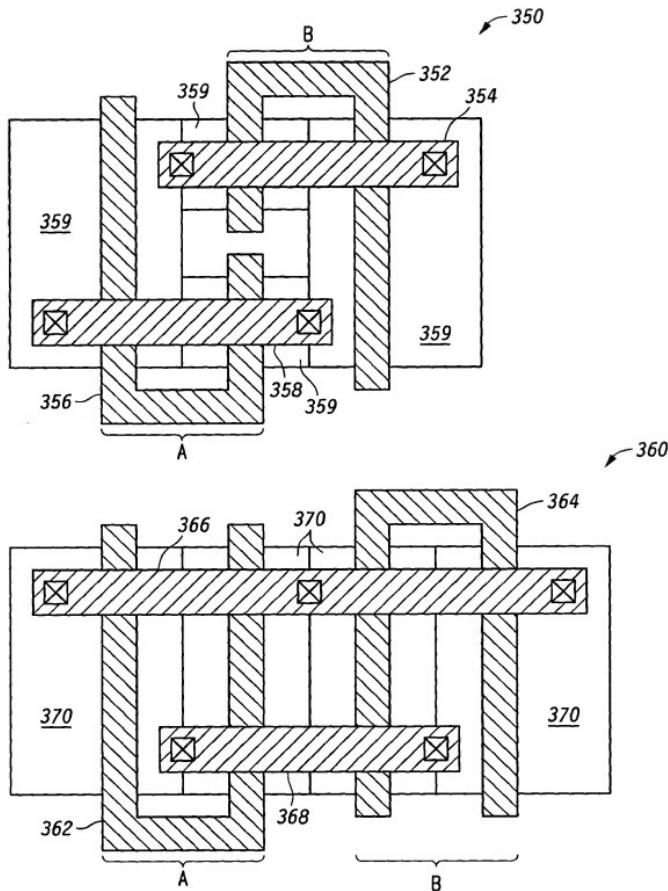


FIG. 13

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- METAL
- DIFFUSION
- POLYSILICON
- ☒ CONTACT

FIG.14

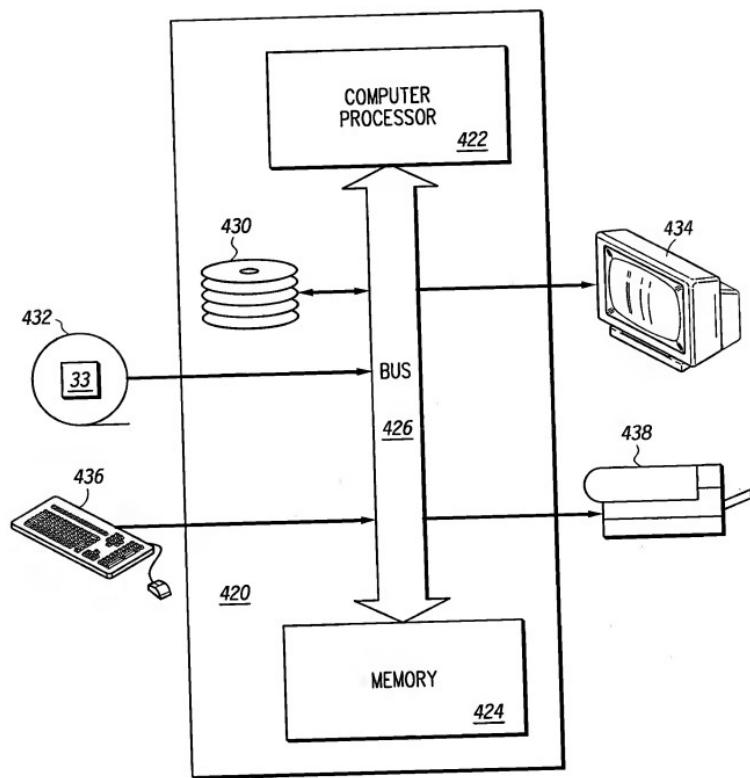


FIG.15

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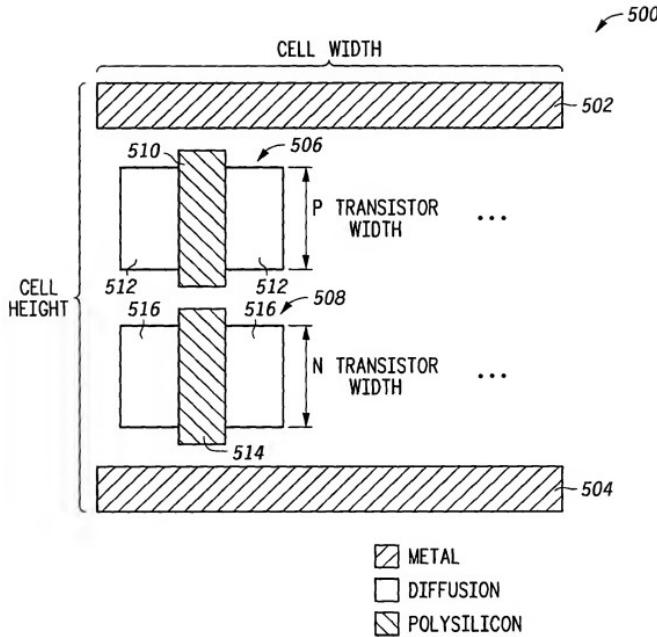


FIG. 16